

	Search Text
1	(system-on-chip or soc or system adj2 chip) and FPGA same (register customer) and @ad<"20011210"
2	(system-on-chip or soc or system adj2 chip) and FPGA same (register customer) and @ad<"20011210" and fpga same bus\$4 and fpga same peripheral
3	(system-on-chip or soc or system adj2 chip) and FPGA same (register customer) and @ad<"20011210" and fpga same bus\$4 and fpga same (peripheral custom\$6)
4	(system-on-chip or soc or system adj2 chip) and FPGA same (register customer) and @ad<"20011210" and fpga same bus\$4 and fpga same (peripheral custom\$6) and local adj bus\$4
5	(system-on-chip or soc or system adj2 chip) and FPGA same (register customer) and @ad<"20011210" and fpga same bus\$4 and fpga same (peripheral custom\$6) and local adj bus\$4 and (FPGA same (user customer))
6	(system-on-chip or soc or system adj2 chip) and FPGA same (register customer) and @ad<"20011210" and fpga same bus\$4 and fpga same (peripheral custom\$6) and local adj2 bus\$4 and (FPGA same (user customer))
7	(system-on-chip or soc or system adj2 chip) and FPGA same (register customer) and @ad<"20011210" and fpga same bus\$4 and fpga same (peripheral custom\$6) and local adj2 bus\$4 and (FPGA same (user customer)) and (base\$2 fixed) near3 (function circuit) and (custom\$4 var\$6) near3 (function circuit)
8	(system-on-chip or soc or system adj2 chip) and FPGA same (register customer) and @ad<"20011210" and fpga same bus\$4 and fpga same (peripheral custom\$6) and local adj2 bus\$4 and (FPGA same (user customer)) and (base\$2 fixed) near3 (function circuit) and (custom\$4 var\$6) near3 (function circuit) and FPGA same (register)
9	(system-on-chip or soc or system adj2 chip) and FPGA same (register) and @ad<"20011210" and fpga same bus\$4 and local adj2 bus\$4 and (FPGA same (user customer)) and (base\$2 fixed) near3 (function circuit) and (custom\$4 var\$6) near3 (function circuit) "."
10	(system-on-chip or soc or system adj2 chip) and (user customer) with fpga with configur\$6 and @ad<"20011210" and fpga same bus\$4 and (base\$2 fixed) near3 (function circuit) and (custom\$4 var\$6) near3 (function circuit)
11	(system-on-chip or soc or system adj2 chip) and (user customer) same fpga same configur\$6 and @ad<"20011210" and fpga same bus\$4 and fpga same embe\$6
12	(system-on-chip or soc or system adj2 chip) and (user customer) same fpga same configur\$6 and @ad<"20011210" and fpga same bus\$4 and fpga same embe\$6
13	(system-on-chip or soc or system adj2 chip) and (user customer) same fpga same configur\$6 and @ad<"20011210" and fpga same bus\$4 and fpga same embe\$6 and custom\$5
14	(system-on-chip or soc or system adj2 chip) and (user customer) same fpga same configur\$6 and @ad<"20011210" and fpga same bus\$4 and fpga same access\$5
15	(system-on-chip or soc or system adj2 chip) and (user customer) same fpga same configur\$6 and @ad<"20011210" and fpga same bus\$4 and fpga same access\$5 and register
16	(system-on-chip or soc or system adj2 chip) and (user customer) same fpga same configur\$6 and @ad<"20011210" and fpga same bus\$4 and fpga same access\$5 and register and customi\$6

	Search Text
17	(system-on-chip or soc or system adj2 chip) same FPGA and dedicated near4 (function circuit) and custom\$6 near3 (function circuit) and @ad<"20011210"